

**IN THE SPECIFICATION**

Please replace paragraph [0005] with the following:

--The disadvantages of a conventional reflective PDP are evident in view of an stripe barrier rib construction as shown in FIGS. 2 and 3. Particularly, barrier ribs 20 as shown in FIG. 2 are aligned sequentially in a column and orthogonal to bus electrodes 14 24. Data electrodes 18 20, which are spaced regularly apart from each other in a direction parallel to the barrier ribs, are placed between the adjacent barrier ribs 22. A discharge (bright) area 26 occurs at the intersection between the data electrode and one of the RGB cells as defined by adjacent sustain electrodes and barrier ribs. A non-discharge (dark) area 27 occurs between the discharge areas, thereby forming a dual scan gap 28 between spaced apart data electrodes extending in a direction along the adjacent sustain electrodes. Transparent or indium-tin-oxide (ITO) part 12 29 of the sustain electrodes extends plasma when adequate voltage applied to the sustain electrode. Additionally, the discharge areas are not effectively utilized due to a dark area in conventional PDP.--

Please insert the following paragraph after paragraph [0016]:

--FIG 5a and FIG 5(b) show the improved plasma display panel with data and sustain electrodes arranged in the closed rib structure of rectangular shape according to a first exemplary embodiment of the present invention.--

Please insert the following paragraph after paragraph [0017]:

--FIG 6a and FIG 6(b) show the improved plasma display panel with data and sustain electrodes arranged in accordance with a second exemplary embodiment of the present invention.—

Please replace paragraph [0019] with the following:

--FIG. 8 and FIG. 8a show ~~[[shows]]~~ the improved plasma display panel with data and sustain electrodes arranged in accordance with a fourth exemplary embodiment of the present invention.--

Please replace paragraph [0022] with the following:

-- FIG. 5 (a) illustrates one exemplary arrangement of the data electrodes in the improved PDP structure of the present invention. Particularly, the column barrier ribs 42 each aligned with a data electrode 50, and the row barrier ribs 44 are each aligned with a sustain electrode 52. The data electrode 50 is preferably of the same width as the column barrier rib 42, and extends under the sub-pixel cell. A dual scan gap (g) 54 is formed between a pair of the spaced part data electrodes 50, each overlapping with the column barrier rib 42 and partially extending on ends thereof under the sub-pixel cells 46. Such partial extension leaves a gap length (d) 56', 56" between one end of the data electrode and one of the row barrier ribs. To avoid image flickering at the dual scan area, the gap length 56' is preferably less than  $0.45p$  (positive value), where  $p$  is the cell pitch length 58 or distance between adjacent row barrier ribs 44 (i.e.,  $d < 0.45p$ ). Additionally, to avoid addressing errors, the gap length ~~[[56]]~~ 56' is preferably less than 40% (positive value), the gap length 56" is negative value that also less than  $0.45p$ . FIG. 5(b) illustrates another exemplary arrangement of the data electrodes in the

improved PDP structure of the present invention. Such partial extension leaves a gap length (d) 56" between one end of the data electrode and one of the row barrier ribs. Wherein that dual scan gap posited under barrier ribs.--

Please replace paragraph [0023] with the following:

-- FIG. 6 illustrates a second exemplary embodiment of the plasma display panel according to the present invention. As show in Fig. 6(a), each data electrode , shown as 60 in Figure 6, may have varying width, depending on its positions in the cell region and the column ribs. Each data electrode has narrow width 50' under column barrier ribs and enlarge width 50" passing under the center portions of cell region. A portion of data electrode under cell region affect addressing ability, wherein that portion of data electrode positioned in dual scan area is designed to be larger than other cell in panel, as show in Fig 6(b).--

Please replace paragraph [0054] with the following:

-- FIG. 8 illustrates a fourth exemplary embodiment of the plasma display panel according to the present invention. In this embodiment, a third dual scan gap 84 is formed between adjacent data electrodes in the column direction. The third dual scan gap has a length that is greater than the cell pitch length 58 and extends over one of the row barrier ribs. To avoid image flickering, the ratio between the cell pitch length (p) and gap length (d) remains the same at less than 45% (i.e.,  $d < 0.45 p$ ).--